

REMARKS

By this amendment, claims 1, 2, 4-7, and 16 have been amended to more clearly recite that which Applicants regard as their invention, and claims 8-11 have been canceled without prejudice to or disclaimer of subject matter contained therein. Support for the changes to claim 1 can be found at, *inter alia*, page 2, lines 15-18, and page 14, line 35 – page 15, line 2, of the specification. Claims 3, 12-15, and 17-23 stand withdrawn from consideration. Claims 1, 2, 4-7, and 16 are presented for further examination.

The objection to claims 1 and 16 and the rejection of claim 1 under 35 U.S.C. § 112, first paragraph, is believed overcome by the foregoing amendments. Specifically, as suggested by the Examiner, the limitation “pluralities of various types of circuit elements including a plurality of circuit elements of for each type” has been replaced with the limitation “various types of circuit elements including a plurality of circuit elements of each type”. Reconsideration and withdrawal of the objection and the rejection are respectfully requested.

The rejection of claims 1 and 2 under 35 U.S.C. § 102(b) as anticipated by U.S. Patent No. 6,271,728 (“Wallace”), and the rejection of claims 4-11 and 16 under 35 U.S.C. § 102(b) as anticipated by or, in the alternative, under 35 U.S.C. § 103(a) as obvious over Wallace in view of U.S. Patent No. 6,287,951 (“Lucas”), are respectfully traversed with respect to the amended claims.

Applicants submit that the structural features of claim 1, as amended, are not disclosed or suggested by either of the cited references. In particular, neither Wallace, which relates to MMIC circuits, nor Lucas, which relates to a method for forming semiconductor device interconnects, teaches an array for producing a Micro Electro Mechanical System (MEMS) device comprising various types of

circuit elements including a plurality of circuit elements of each type and switches for connecting each of said circuit elements, wherein ***at least one of said switches are connected on each side of said circuit elements*** and some of said circuit elements are interconnected by determining open or close of all of said switches so as to make a circuit.

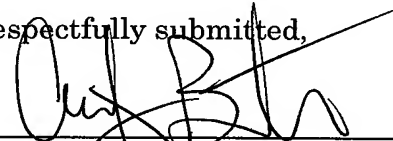
In view of the foregoing, the application is respectfully submitted to be in condition for allowance, and prompt favorable action thereon is earnestly solicited.

If there are any questions regarding this amendment or the application in general, a telephone call to the undersigned would be appreciated since this should expedite the prosecution of the application for all concerned.

If necessary to effect a timely response, this paper should be considered as a petition for an Extension of Time sufficient to effect a timely response, and please charge any deficiency in fees or credit any overpayments to Deposit Account No. 05-1323 (Docket #101249.55749US).

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Respectfully submitted,



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